Building a High-Assurance Separation Kernel using Programatica

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The Oregon Separation Kernel:



A working μ -kernel implementation with very high assurance of separation between domains

(interference only as permitted by explicit policy)

- Good design, good architecture
- Sound engineering, informal reasoning
- Cope with an abundance of small details
- Rely on behavior of underlying platform
- The Programatica Approach: "Programming as if properties matter"

- Good design, good architecture
 - Reuse the good ideas!
 - Monads, ADTs: "separation by construction"

- Good design, good architecture
- Sound engineering, informal reasoning
 - Capture specifications/programmer expectations as embedded properties ("Extreme Formal Methods")
 - Integrate with (formal & informal) validation tools

- Good design, good architecture
- Sound engineering, informal reasoning
- Cope with an abundance of small details
 - Raise the level of abstraction
 - Leverage types: "mostly types, a little proving"

- Good design, good architecture
- Sound engineering, informal reasoning
- Cope with an abundance of small details
- Rely on behavior of underlying platform
 - "Trusted hardware" ... but trusted to do what?
 - Formalize and document assumptions

Ingredients:

Programatica:
 Certification

Haskell:

Modeling, implementation, tractable reasoning

- L4:

Keeping it real

House:

Feasibility, prototyping

Programatica

Programatica Goals:

- Develop methodologies, tools, and foundations to support the construction and certification of highassurance systems
- Integrate a broad and open spectrum of assurance techniques (code review, testing, formal methods, ...)
- Support evolving code, evidence, and assurance requirements (e.g., track dependencies, revalidate, ...)
- Apply to assurance of security properties in complex software artifacts of engineering significance

The Programatica Vision:



The Programatica Browser:



Programatica Servers:

"I say so" A person signs their name by an assertion Test **Testcases** Individual test cases / regression testing implemented, QuickCheck 🗣 automated, Random testing maturing Plover The P-logic verifier Alfa A Interactive proof editor based on type theory Isabelle hand translation Logical framework, tactic-based theorem prover Model Checking of Monadic Code new development

Early Case Study:

Based on a Hypothetical Crypto Chip Design



- Modeled in Haskell (~260 LOC)
- GUARANTEED separation between channels

The Separation Property:



```
assert Separation
= All algs :: Algs.
All select :: (ChannelId → Bool).
{ filter (select . fst) . chip algs }
===
{ chip algs . filter (select . fst) }
```

Concluded with formal proof using the Alfa server

Haskell

Haskell:

- An expressive, purely functional programming language
- A semantically rich, formal modeling language
- A "(semi-) formal method"

Design Document:

Or course, both (γ) and (ω) interit the associativity and commutativity of the interlying (se) and (1) operations on the Haskell β_{log} type. $:: Perms \rightarrow Perms \rightarrow Bool$ (=) $p \sqsubseteq q = (readable \ p \le readable \ q)$ as (w(To see how this works, it might be useful to point smart CombatePermed.save $= \forall op : Associative op \implies Associative \{ combinePerms op \}$ standard Haskell ordering on Booleans.) It is easy to see that this is not a total ordering It is easy to see that this is not a total original and writePerm are incomparable. (As a result, define the ordering as an instance of the Haskef Commutative op => Commutative{ Vop. Commutative op => Commutative{ consistePerms op} operator does satisfy the laws for a partial orde Severt Intersect PermaAssoc sussort PermsPartialOnter = Par assort UnionPermsAssor The "smallest" and "largest" values with respe insert IntersexPermsCommutative seart UnionPermsCommutative by noPerms and fullPerms, respectively; Associative (17) The (T) operation can be used to ensure that permissions (x, y)when an object is framed from one thread to income (x, y)have permission y on y rescues thread (x) structure. It is sending thread thread, then the truth permission the tro y with permissions y to another (forcentially smaller) permission value T_{eff} of course, this is far y to another (see where $p \in f_{eff}$) with in turn follows vacuously in the further speed case where p is faithermat. Associative (U) noPerms, fullPerms :: Perms noPerma fullPerms Permst -Perms [res issort NoPermsBottom assert FullPermsTop $\forall p , T$ $\forall p$ It follows, by a simple application of antisy are incleed the smallest and the largest p assert NoPermsSmallest $\begin{array}{l} \text{inscrit}\ \textit{InternactPermsOrder} \\ &= \ \forall p,\ q.\ \textit{True}\{\ q \subseteq p\} \Longrightarrow (\{p \cap q\} = = q) \end{array} \end{array}$ $= \forall p . True \{ p \subseteq noPerms \}$ $\begin{array}{rcl} \text{smacrt hateraactParmsFullParms} \\ &= & \forall q \cdot (fallParms \cap q) = = & q \end{array}$ assert FullPermsLargest = ∀p. True{ fullPerms ! Unsurprisingly; each of these properties has a corresponding dual for (u): Permissions can be combined by oper remissions can be contonied by ope-that are defined in terms of a gene "and" and "or", respectively: $\begin{array}{rcl} \text{Assort } Un(anPerm_{d}UpperBound \\ &= & \forall p, \ q. \ True \{ \ p \subseteq (p \sqcup q) \} \end{array}$ infixr 5 ⊓,⊔ $\begin{array}{rcl} \text{insert } UnionPermiGoder\\ &=&\forall p, q. \ True \{ q \subseteq p \} \rightsquigarrow (\{p \sqcup q\} ===p) \end{array}$ (E), (L) combinePerms op p1 p2

Bare Metal: A Programatica Mo

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Abstract This document presents a high-level, abort vace platforms that includes features much control node securitor. This model was aber source of cultural security properties. Our the Programmics approach to "program containing executable Hashell cube and for a single source document.

1 Introduction

This document presents a high-level model includes features such as virtual memory is o'reign separation (Kerne¹⁷), which is a veloping. As such, the model focuses on ufor the implementation of Osker and interu any specific architecture. A keg goal of the aness of critical scenuty properties, include domains and/or virtual machines that at

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Executable Model:

C:\Program Files\Hugs98\hugs.exe	
II III IIII III III	
Type :? for help DomOS3> main A: Demo starting A: Receiver: my total is 0 A: Receiver: my total is 0 A: Receiver: my total is 1 A: Receiver: my total is 1 A: Receiver: my total is 3 A: Receiver: my total is 3 A: Receiver: my total is 6 A: Sender: I just sent 4 A: Receiver: my total is 10 A: Sender: I just sent 5 A: Receiver: my total is 15 A: Receiver: my total is 15 A: Receiver: my total is 21 A: Sender: I just sent 7 A: Receiver: my total is 28 A: Receiver: my total is 36 A: Sender: I just sent 9 A: Receiver: my total is 45 A: Receiver: my total is 55 no runnable domains	
	-

Why Haskell?

- Purity: the result of a function depends only on the argument value (i.e., no hidden dependencies)
- Polymorphic types: powerful and expressive; parametricity provides "theorems for free"
- Formal semantics: a foundation for meaningful assurance guarantees
- Powerful abstract datatypes: e.g., modular, scalable encapsulation and reasoning about effects using monads

Scalability:



Note: Diagram not to scale ... \odot

Future Prospects:

- Performance is not a primary goal ... but it is an issue:
 - Paths through a μ -kernel must be short and fast
 - Runtime system assurance: e.g., garbage collection
- On the table:
 - Mechanisms for efficient construction and manipulation of data structures at the bit-level
 - Small size provides opportunities for aggressive optimization and whole program analysis
 - Default to strict instead of lazy evaluation

L4

What is L4?

- L4 is a "second generation" μ-kernel design
- Original Design: Jochen Liedtke
 - Original goal: To show that μ-kernel based systems are usable in practice with good performance
- Keep it simple:
 - Original API had just 7 system calls dealing with key abstractions:
 - Address spaces: Memory protection
 - Threads: Concurrency
 - IPC: Inter Process Communication

- L4 is industrially and technically relevant
 - Multiple working implementations (Pistachio, Fiasco, etc...)
 - Multiple supported architectures (ia32, arm, powerpc, mips, sparc, ...)
 - Already used in a variety of domains, including real-time, security, virtual machines & monitors, etc...

- L4 is industrially and technically **relevant**
- L4 is small enough to be tractable
 - Original implementation ~ 12K executable
 - Recent/portable/flexible implementations ~ 10-20 KLOC C++

- L4 is industrially and technically relevant
- L4 is small enough to be **tractable**
- L4 is real enough to be interesting
 - For example, we can run multiple, separated instances of Linux (specifically: L4Linux, Wombat) on top of an L4 μ-kernel

- L4 is industrially and technically relevant
- L4 is small enough to be **tractable**
- L4 is real enough to be interesting
- L4 is a good representative of the target domain and a good tool for exposing core research challenges
 - Threads, address spaces, IPC, preemption, interrupts, etc... are core μ-kernel concepts, regardless of API details
 - It should be possible to retarget to a different API or μ-kernel design

House

An OS in Haskell!?

- OS implementations involve:
 - Iow-level data structure manipulation, "bit twiddling"
 - asynchronous interrupts, MMU, DMA, IO ports, …
- Haskell may not be your "typical systems programming language" ...
- But details like these are within reach ...

Page Table Maintenance:

- type PAddr -- physical addresses
- type VAddr -- virtual addresses
- type PageMap -- page map references

setPage :: PageMap \rightarrow VAddr \rightarrow Maybe PageInfo \rightarrow H Bool getPage :: PageMap \rightarrow VAddr \rightarrow H (Maybe PageInfo)

assert {do setPage pm va pi; getPage pm va}
 ===
 {do setPage pm va pi; return pi}

House:

House	Run a.out executables Page fault and syscall handlers Haskell window system and applications Cooperating concurrent processes
Haskell Device drivers (keyboard/mouse/text vid	
GHC RTS C	Concurrent threads Asynchronous exceptions Garbage collection
hOp _C	Hardware interrupts/faults
x86	

On Bare Metal:



Relating Osker & House:



Different Shapes ⇔ Different Interfaces



Interface = Signature + Properties

Modular Construction:





Modular Construction:



execContext :: PageMap \rightarrow Context \rightarrow H (Interrupt, Context)

assert All m, pm, pa, c.
 m ::: NotMapped pm pa
==> m ::: Commutes {readPhys pa} {execContext pm c}

Modular Certification:



- H = Properties of HW model
- U = Properties of Userspace interface
- S = Osker separation properties
- X = Properties of x86 hardware

- Compositional certification
- Consistency checking on U
- Design input on X

Combining Osker & House:



A First Implementation of Osker on Bare Metal

Standard C code, ...

```
#define wait 1
                                            void sender() {
#define sync 2
                                               LOCK(printf("I am the sender\n"));
                     #defines
#define src 3
                                               int i;
                                               for (i = 0; i<10; i++) {
#define dest 4
                                                 setMsg(i);
extern void lock(), sender(), receiver();
                                                 send(dest, wait);
                                                 LOCK(printf("I just sent %d\n", i));
void start() {
   fork(sync, lock);
                                               stop();
   fork(src, sender);
   fork(dest, receiver);
                                            void receiver() {
   stop();
                                               int total = 0;
}
                        Osker
                                               LOCK(printf("I am the receiver\n"));
                    system calls
                                               for (;;) {
void lock() {
   for (;;) {
                                                  int x;
     recv(0, wait);
                                                  int s;
     send(getSender(), wait);
                                                  recv(src, wait);
                                                  x = qetMsq();
                                                  s = getSender();
}
                                                                      printf(
                                                  total += x;
#define LOCK(x) send(sync, wait); \
                                                  LOCK(printf("Received %d from %d,
                                                        total is %d\n", x, s, total));
                X; \setminus
      malloc(), protected execution
                                               stop();
   (divide by zero, segment violation,
      time slice exhausted, etc...), ...
```

Standard tools, ...

}

}

#define wait 1 #define sync 2 #define src 3 #define dest 4 extern void lock(), sender(), receiver(); void start() { fork(sync, lock); fork(src, sender); fork(dest,receiver); stop(); } void lock() { for (;;) { recv(0, wait); send(getSender(), wait); 3 } #define LOCK(x) send(sync, wait); \ x; \ recv(sync, wait)

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int total = 0; LOCK(printf("I am the receiver\n")); for (;;) { int x; int s; recv(src, wait); x = getMsg();s = getSender(); total += x; LOCK (printf ("Received %d from %d, total is %d\n", x, s, total)); 3 stop();



Compile, boot, and run:

ł

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 }
 stop();







One Source, Many Uses:

Design Document:

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	-

Certification Target:



Practical Implementation:

Explode!				
		Calcu.	lator	·
Terminal 2	- 0			
Welcome to House!	7	8	9	+
> 1s -1				
Module count: 3	4	5	6	-
1: /sample [0x003380000x0039c020]	1	2	3	*
2: /alt [0x0039d0000x003a1020]				
· · · · · · · · · · · · · · · · · · ·	C C	0	=	
Terminal 1				
Counter I am the receiver I am the received I rom 3, total is 0 I just sent 1 I just sent 1 I just sent 2 I just received 1 from 3, total is 3 I just sent 3 I just sent 3 I just sent 4 I just received 2 from 3, total is 6 I just sent 5 I just received 4 from 3, total is 10 I just sent 6 I just received 5 from 3, total is 15 I just sent 7 I just sent 7 I just sent 8 I just sent 8 I just sent 9 I just sent 9 I just sent 9 I just sent 4				

One Source, Many Uses:

Our Design Document is also **Our Executable Model** and also **Our Certification Target** and also **Our Running Implementation**

Why "House"?

 the "Haskell Users Operating System Environment"

Why "House"?

• You are more secure in a House ...



... than if you only have Windows ...

Next Steps

Next Steps:

- OS Model: Continuing transition to a more accurate/more complete (and more complex) L4 API
- Hardware Model: Extensions to describe interrupts and hardware concurrency mechanisms
- Establish formal separation property
- Continued evolution of bare metal implementation



Increasing RTS Assurance:

- House illustrates that we can run Haskell programs on a very thin OS layer, obeying a small set of properties
- The runtime system (RTS) is large, complex, and written in C, which makes it hard to build confidence in the overall system
- We need high-confidence versions of two main services:
 - Pre-emptive concurrency (needed for interrupt handling)
 - Garbage collection (possibly real-time)

Possible approaches to highconfidence concurrency:

- Model RTS in Haskell
 - Prove key properties about the model;
 - Transfer results back to C code.
 - (The Galois "Haskell on Bare Metal" project is pursuing this.)
- Remove pre-emptive concurrency from the RTS:
 - Leverage Osker concurrency, handle interrupts explicitly
 - Use a language subset for which we can accurately bound execution times

Possible approaches to highconfidence garbage collection:

- Develop ad-hoc proof of correctness for conventional GC using recently developed separation logics.
- Rewrite the Osker model in a language variant with a region-based type system
 - Should require only simple RTS ⇒ relatively easy to validate